

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination DONG ET AL.	
		Examiner Suk-San Foong	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-5,619,052	04-1997	Chang et al.	257/321
B	US-5,866,474	02-1999	Liu, Yi-Chih	438/591
C	US-6,180,543	01-2001	Yu et al.	438/787
D	US-5,403,786	04-1995	Hori, Takashi	438/585
E	US-5,726,087	03-1998	Tseng et al.	438/261
F	US-6,096,630	08-2000	Byun et al.	438/592
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	Wolf S. "Silicon Processing for the VLSI-ERA: Volume 1-Process Technology", 1986, Lattice Pr., Vol. 1, p. 441
V	Hori, T. "Electrical and Physical Properties of Ultrathin Reoxidized Nitrided Oxides Prepared by Rapid Thermal Processing", Feb. 1989, IEEE Transactions on Electron Devices, Vol. 36 No. , p. 340-350
W	
X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.